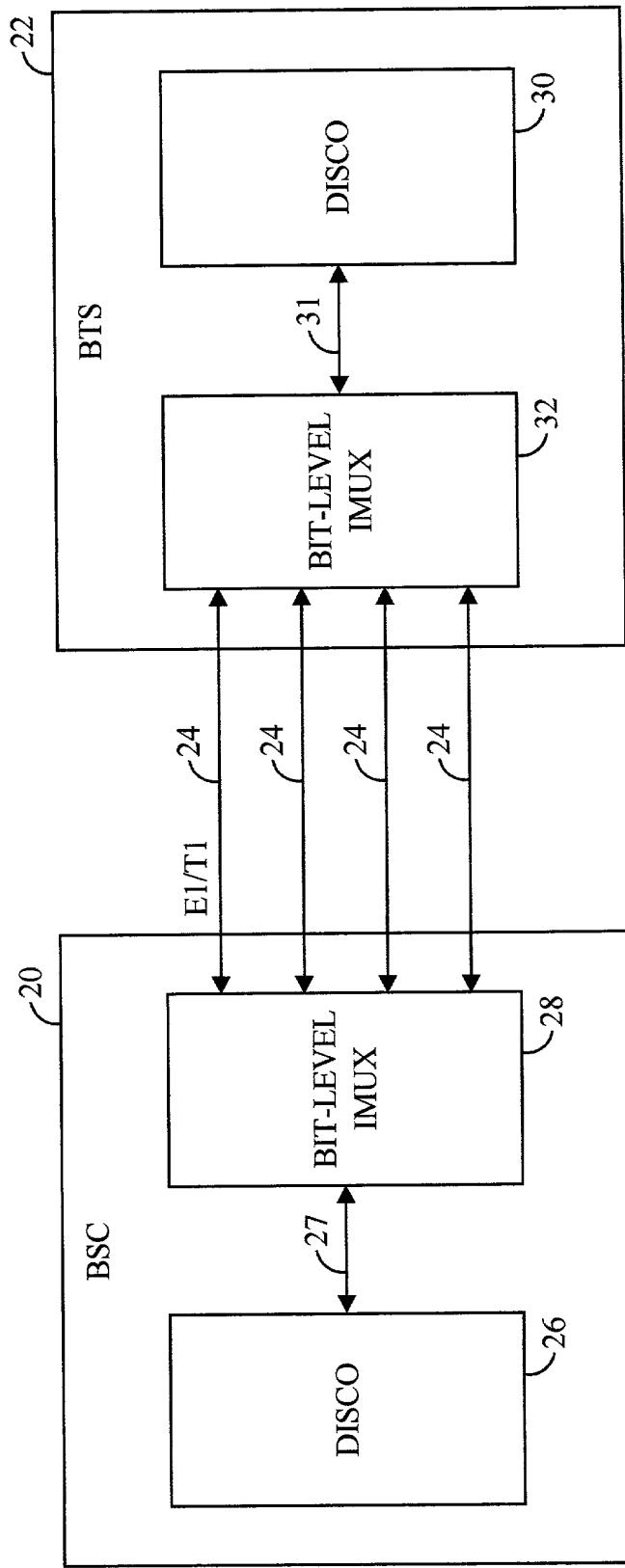


FIG. 1

DISCO 30  
DISCO 32  
BIT-LEVEL IMUX 31  
BIT-LEVEL IMUX 32  
E1/T1 24  
E1/T1 24  
E1/T1 24  
E1/T1 24  
DISCO 26  
BIT-LEVEL IMUX 27  
BIT-LEVEL IMUX 28



(PRIOR ART)

FIG. 2

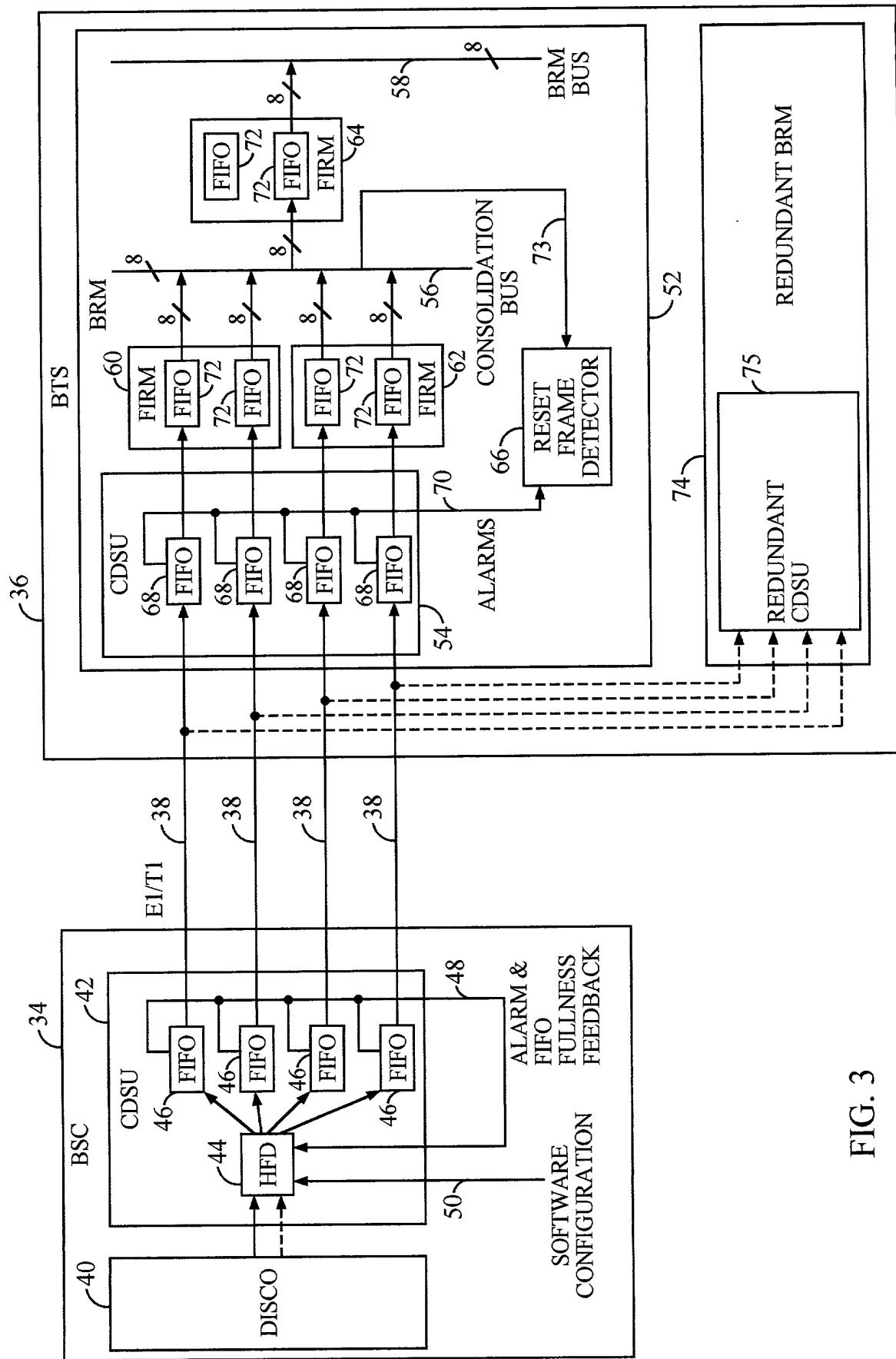


FIG. 3

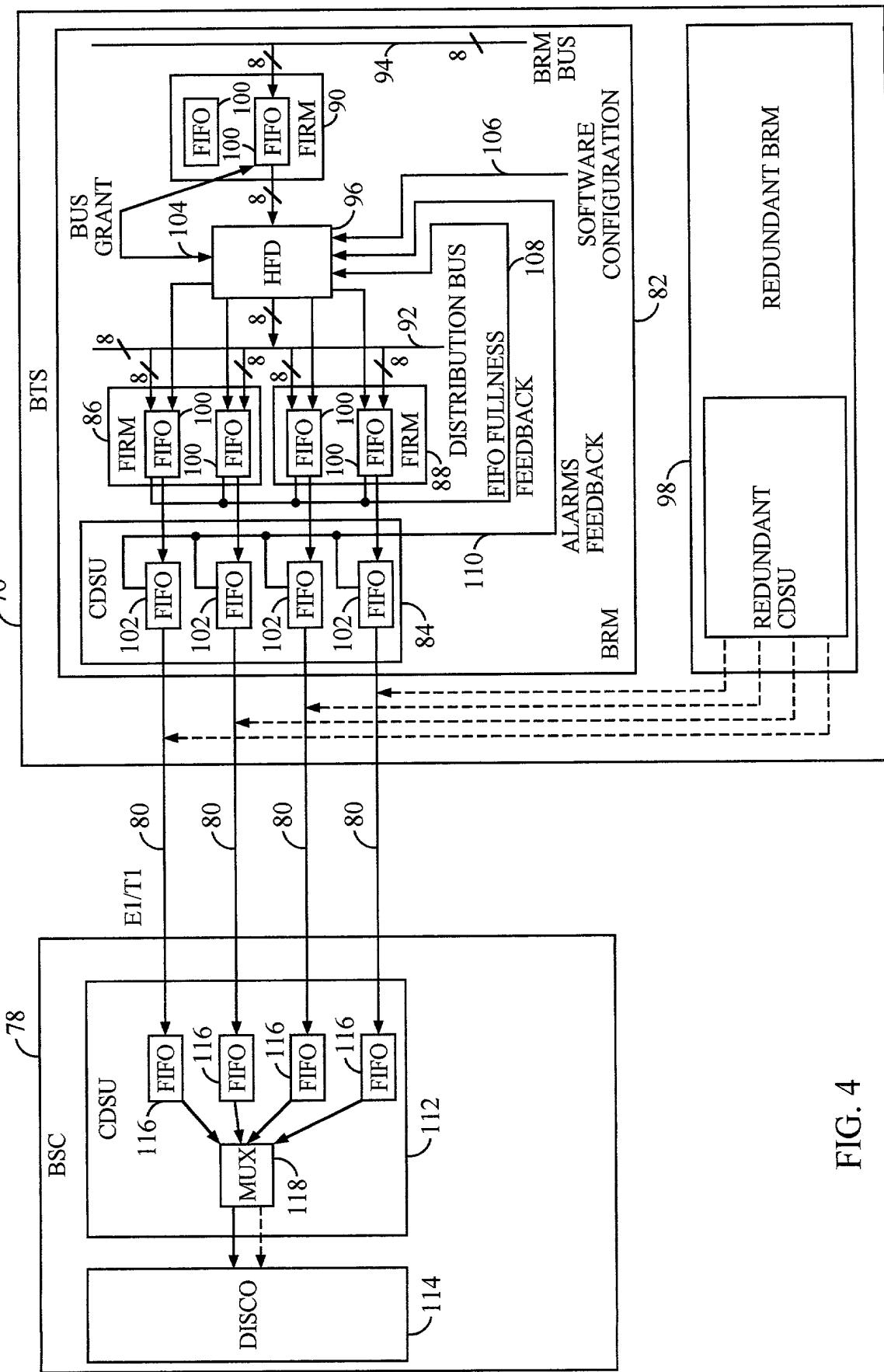


FIG. 4

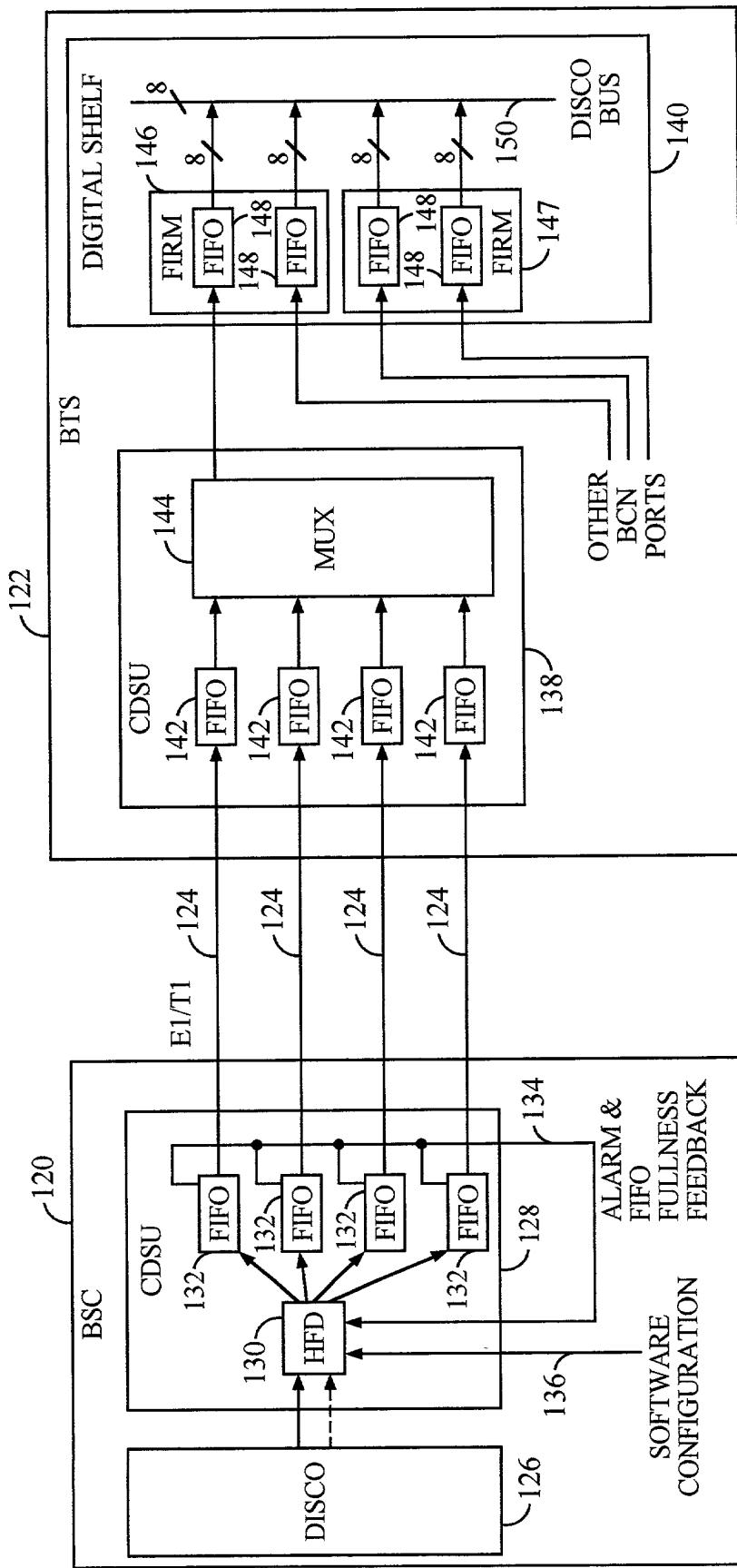


FIG. 5

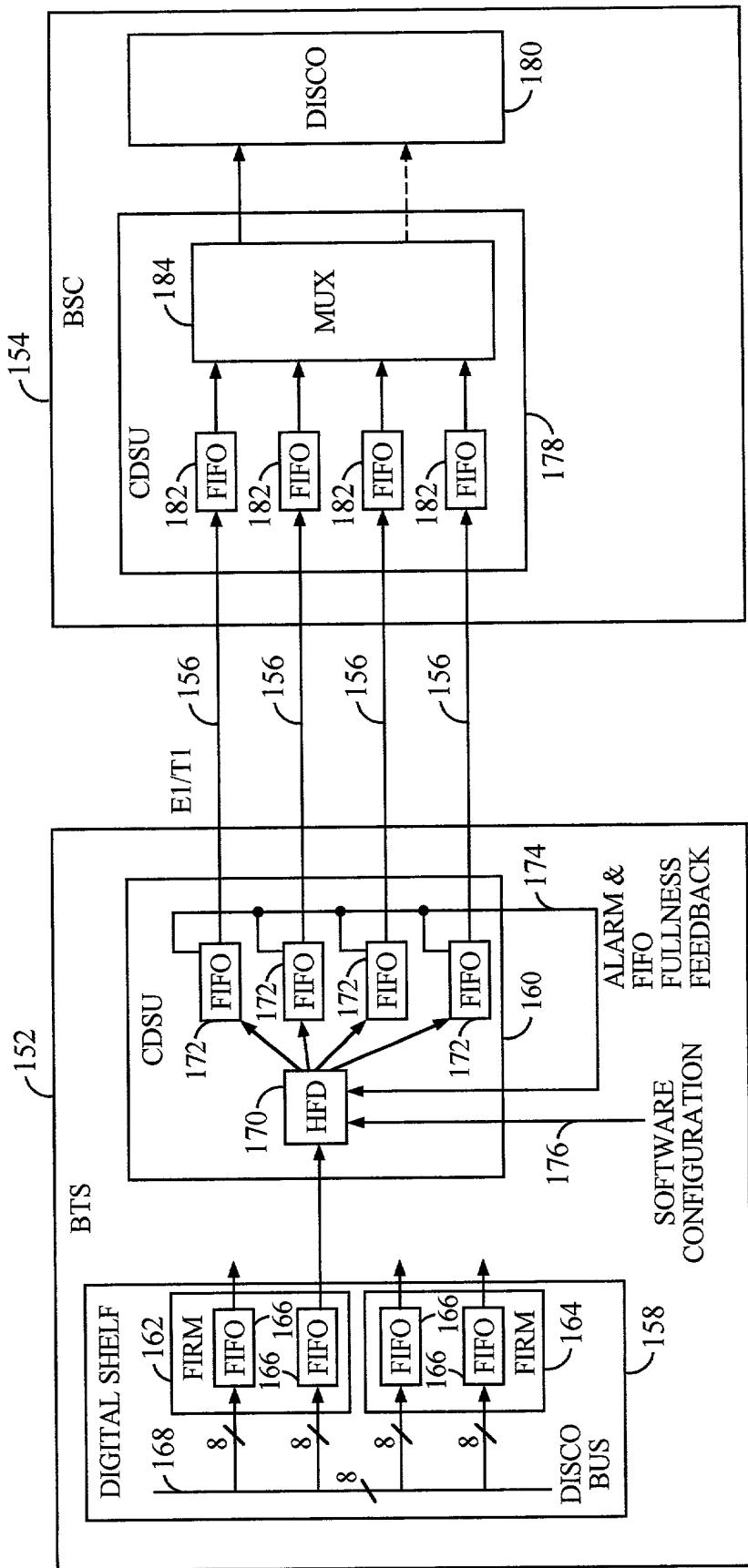


FIG. 6

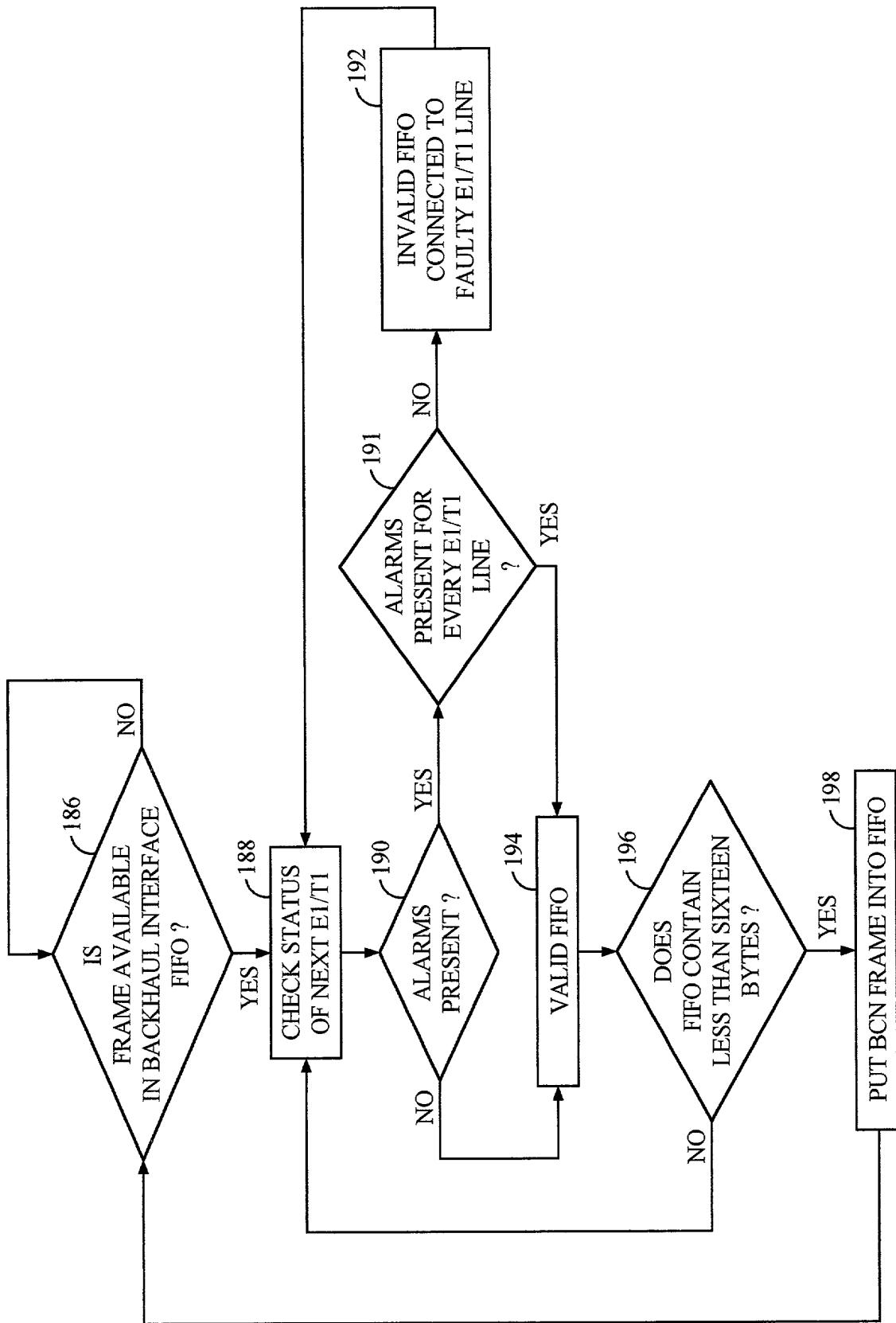


FIG. 7

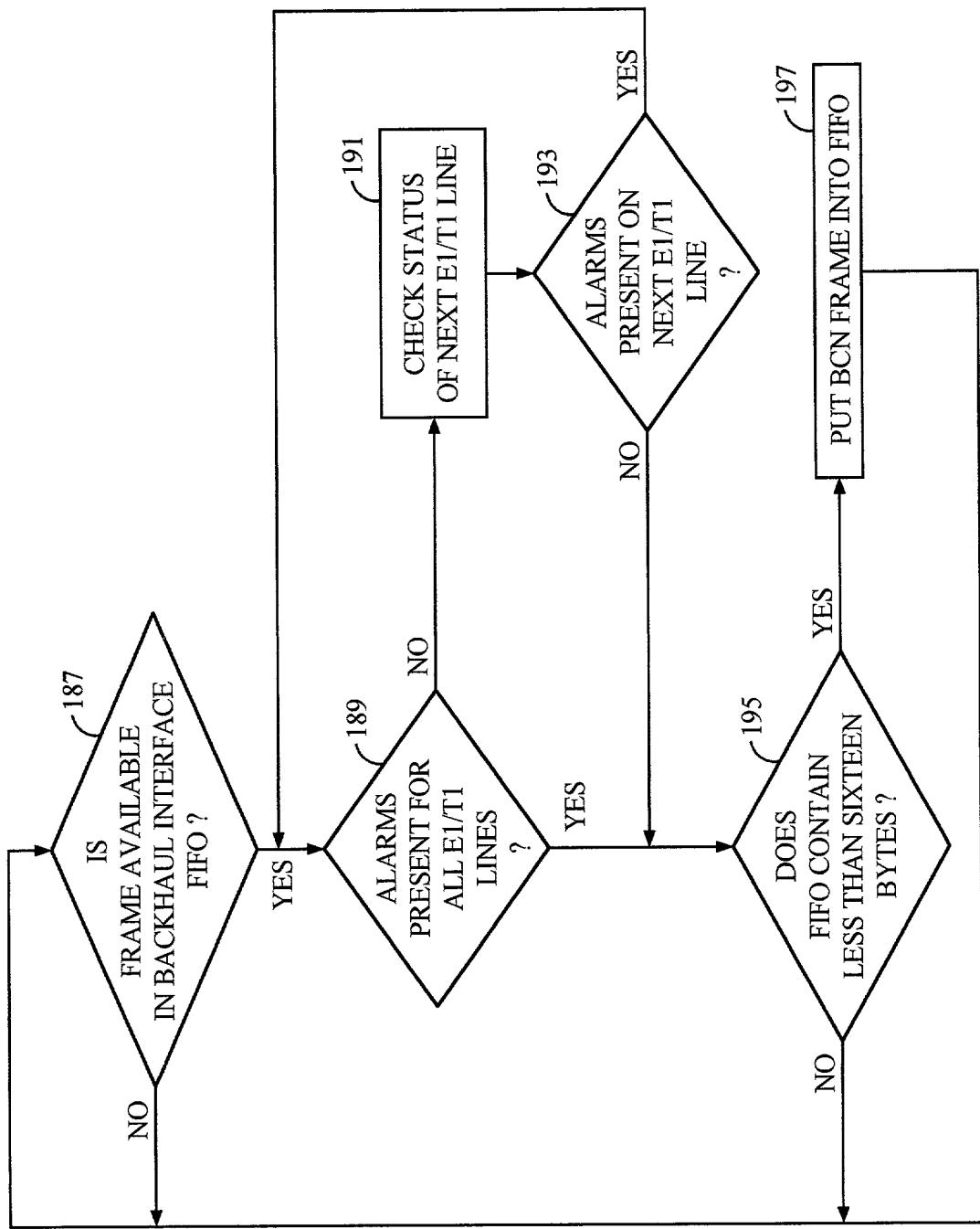


FIG. 8

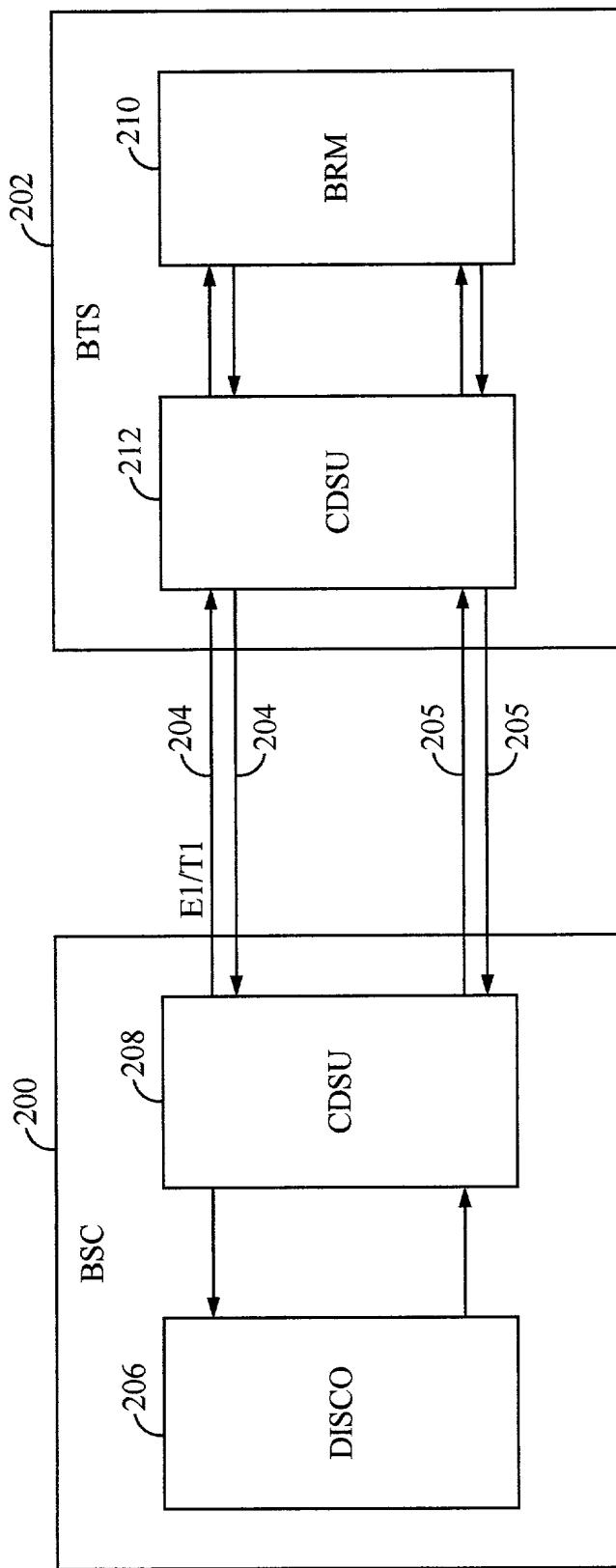


FIG. 9

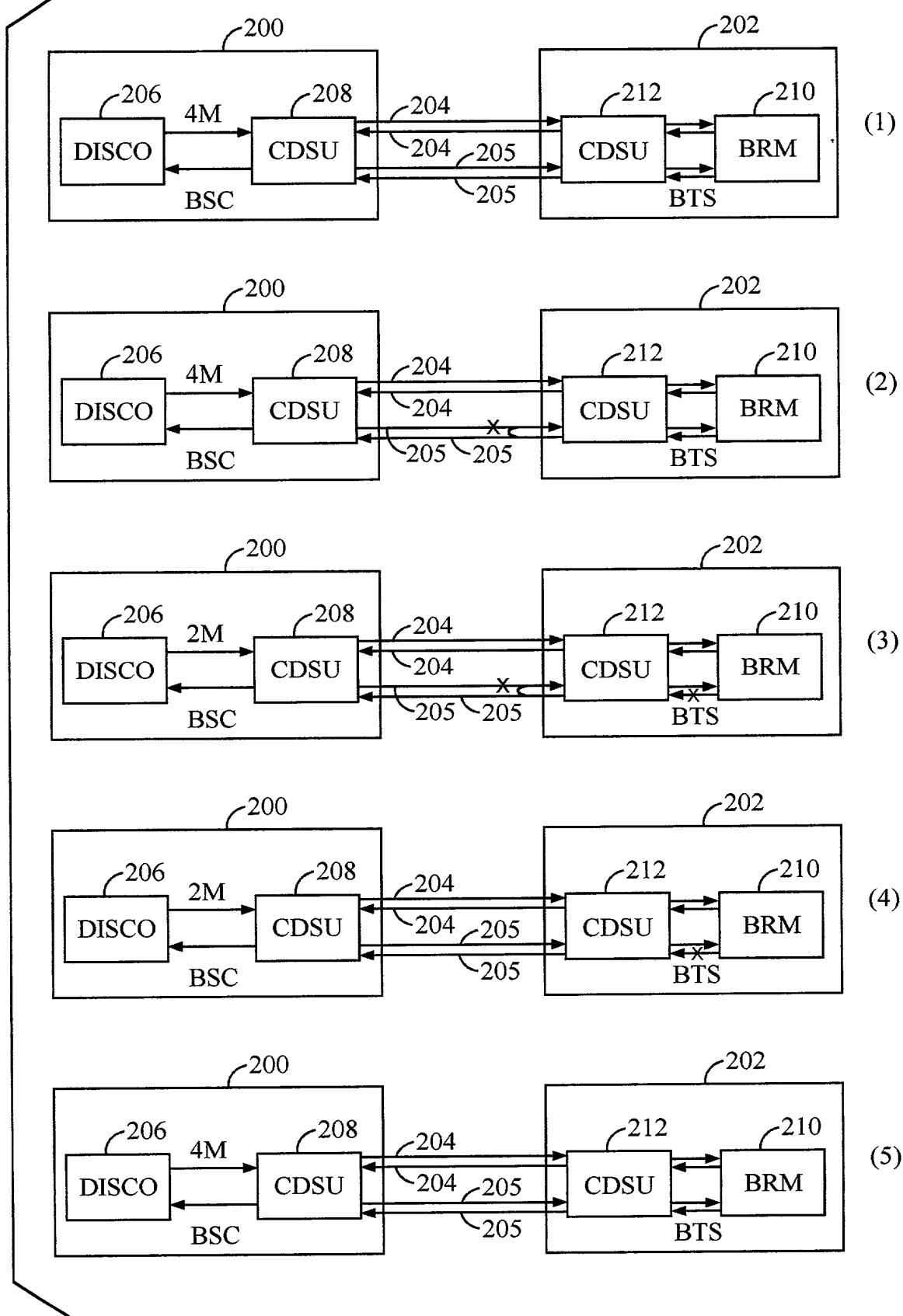


FIG. 10

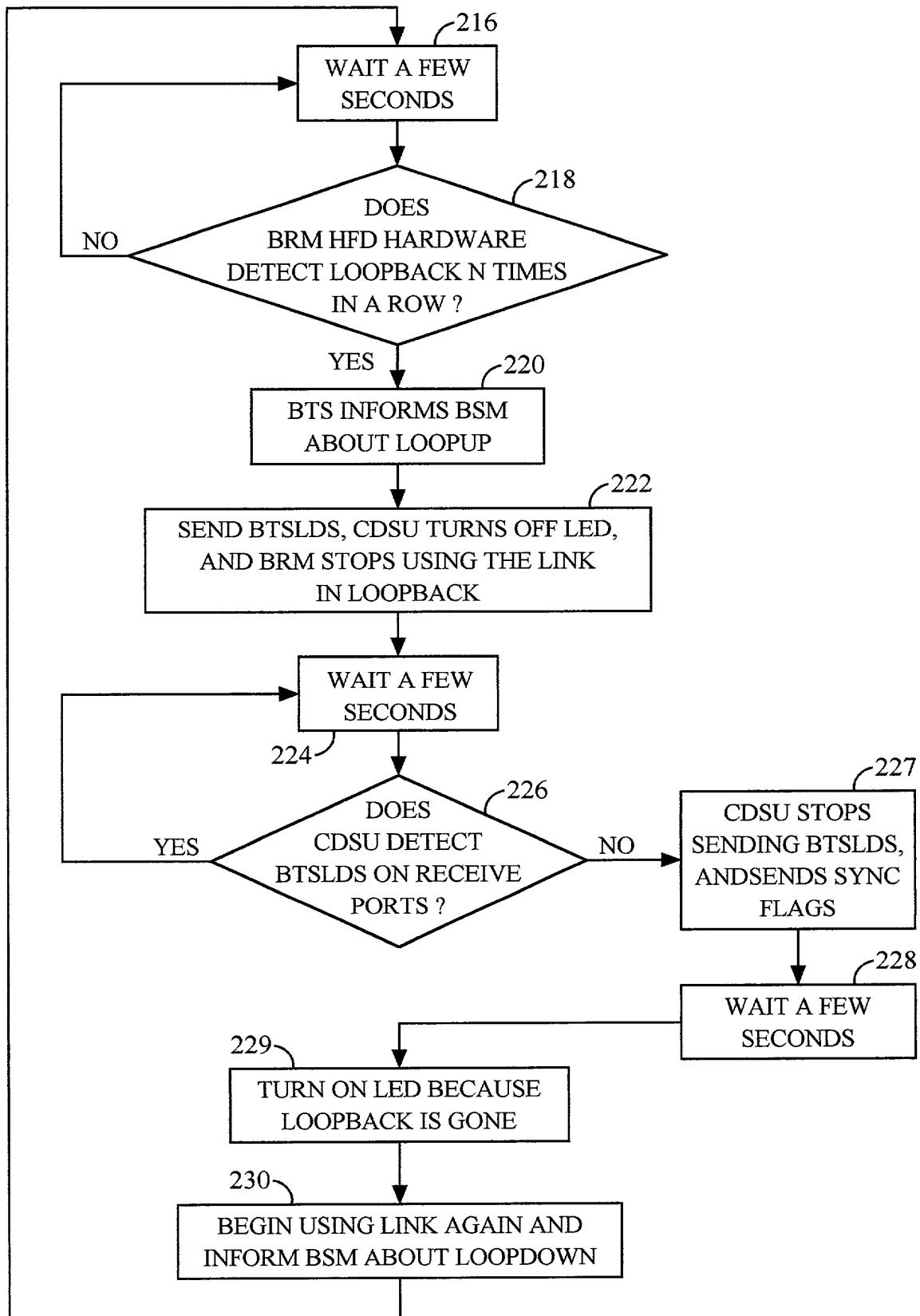


FIG. 11

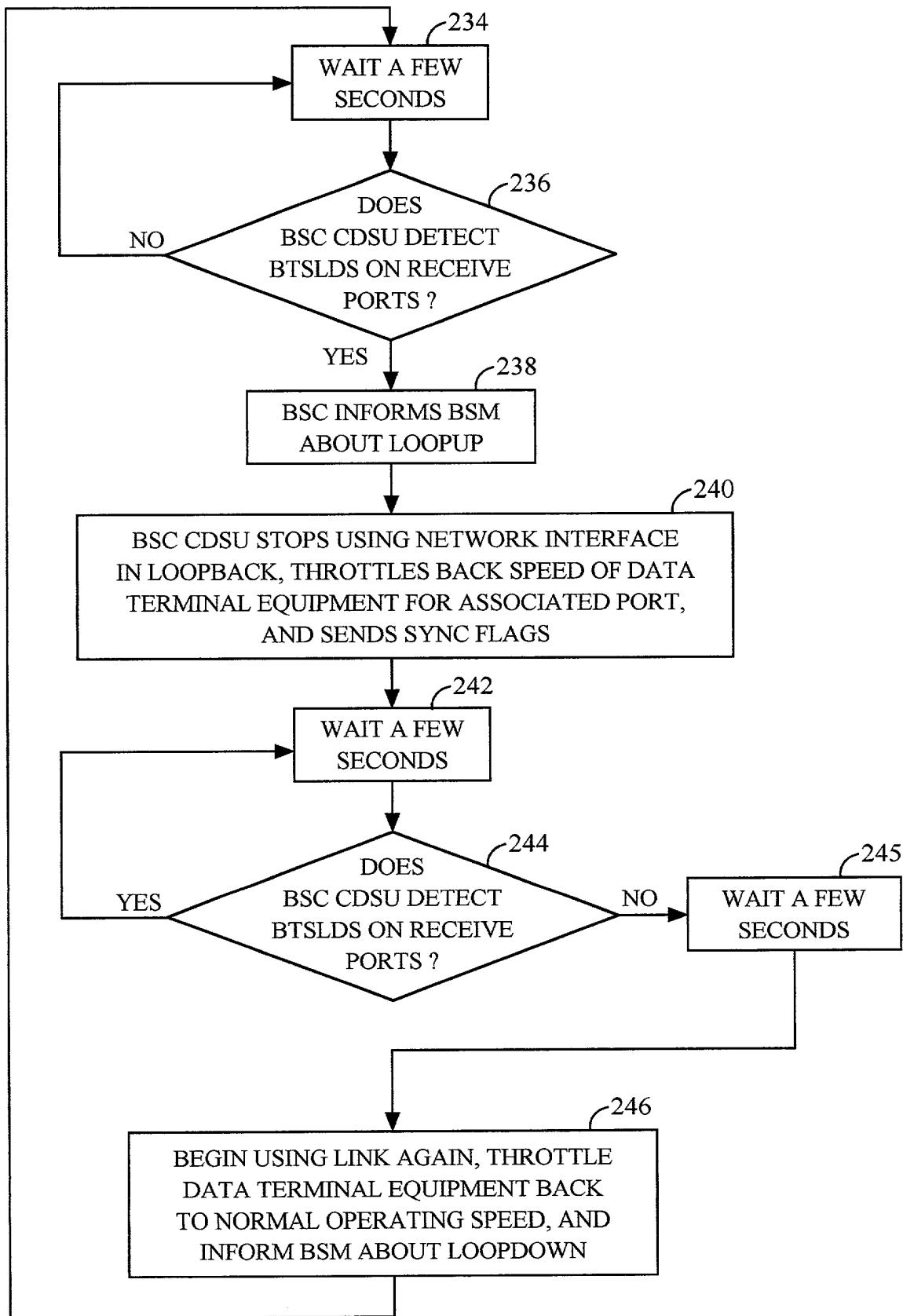


FIG. 12

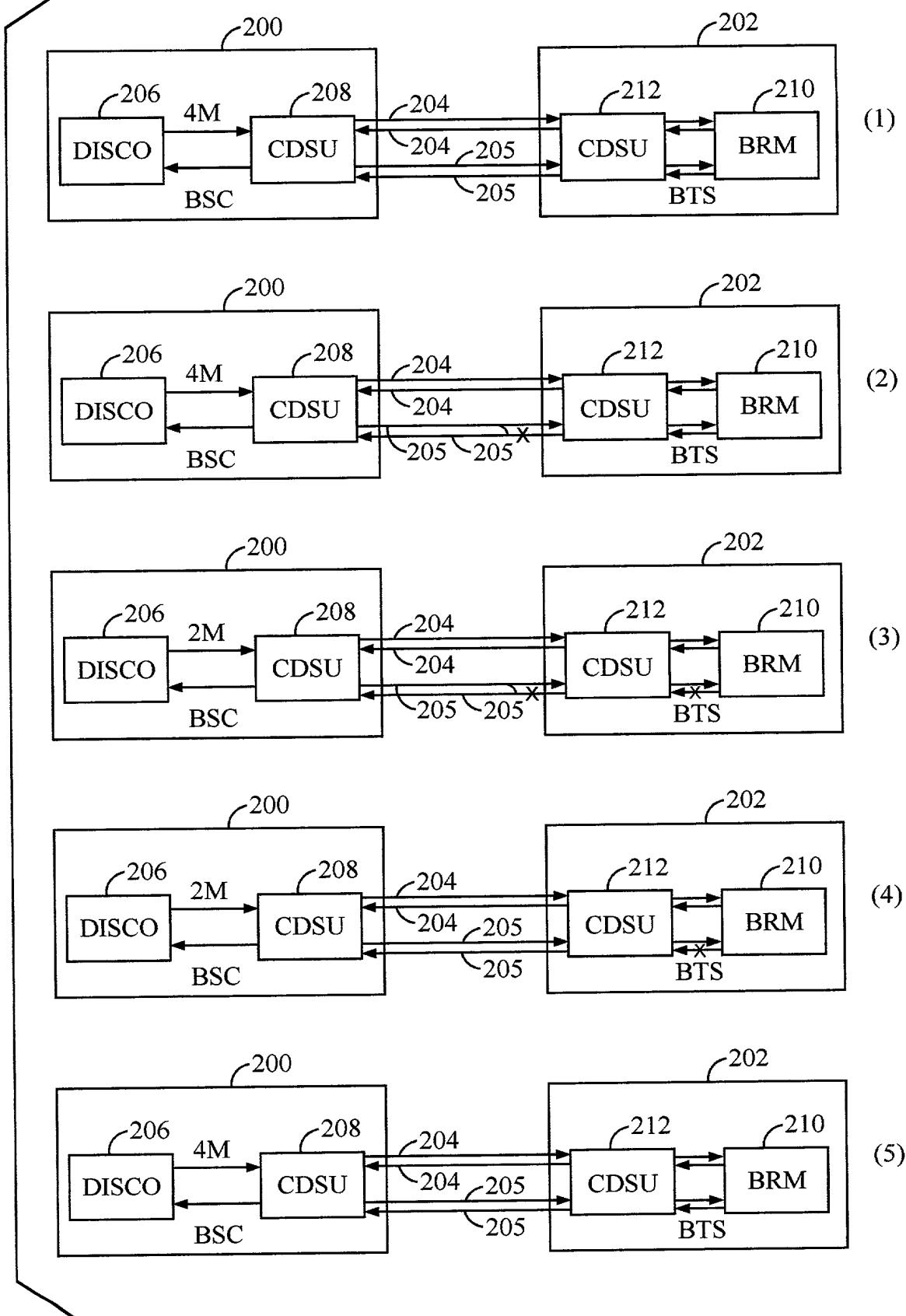


FIG. 13

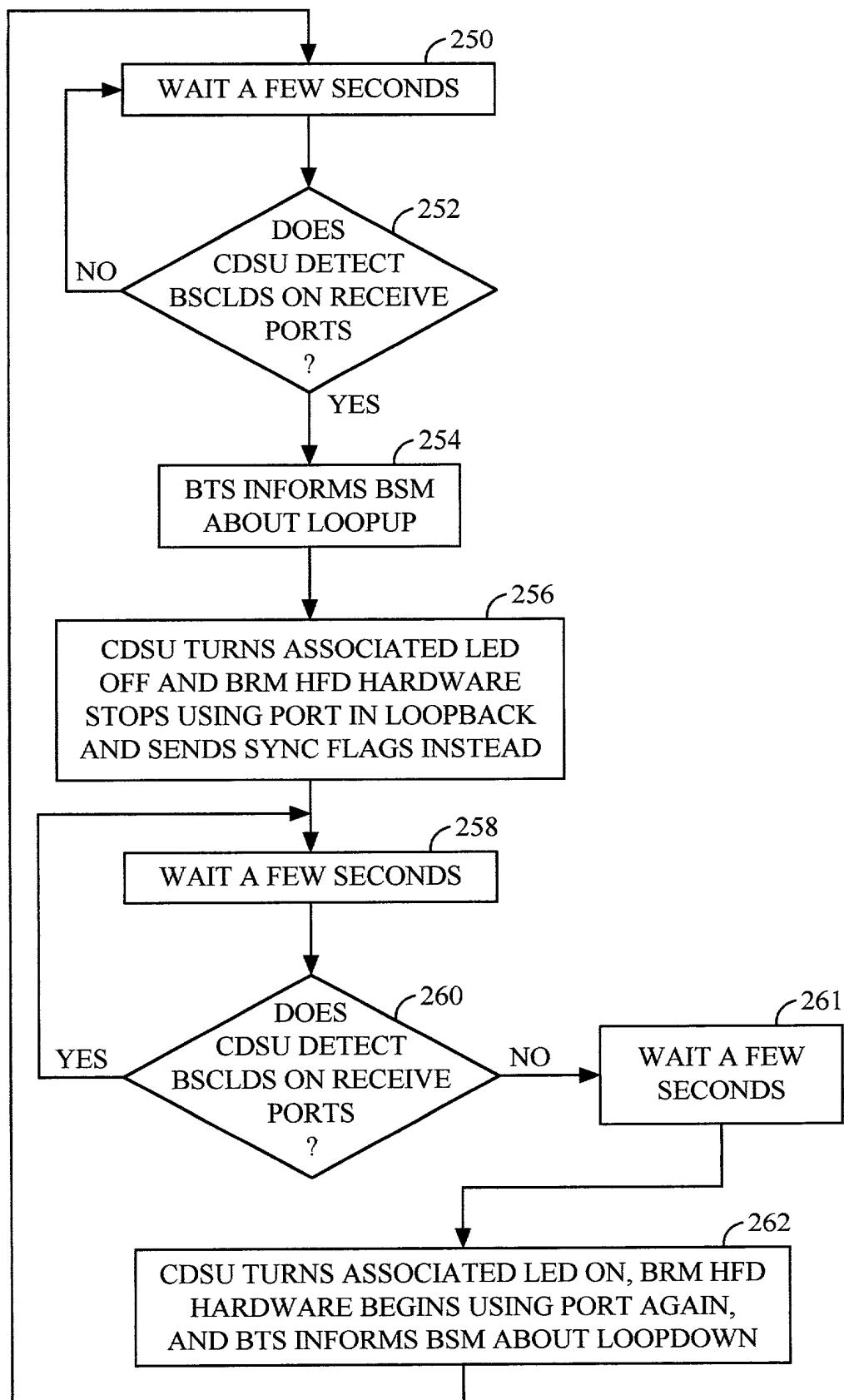


FIG. 14

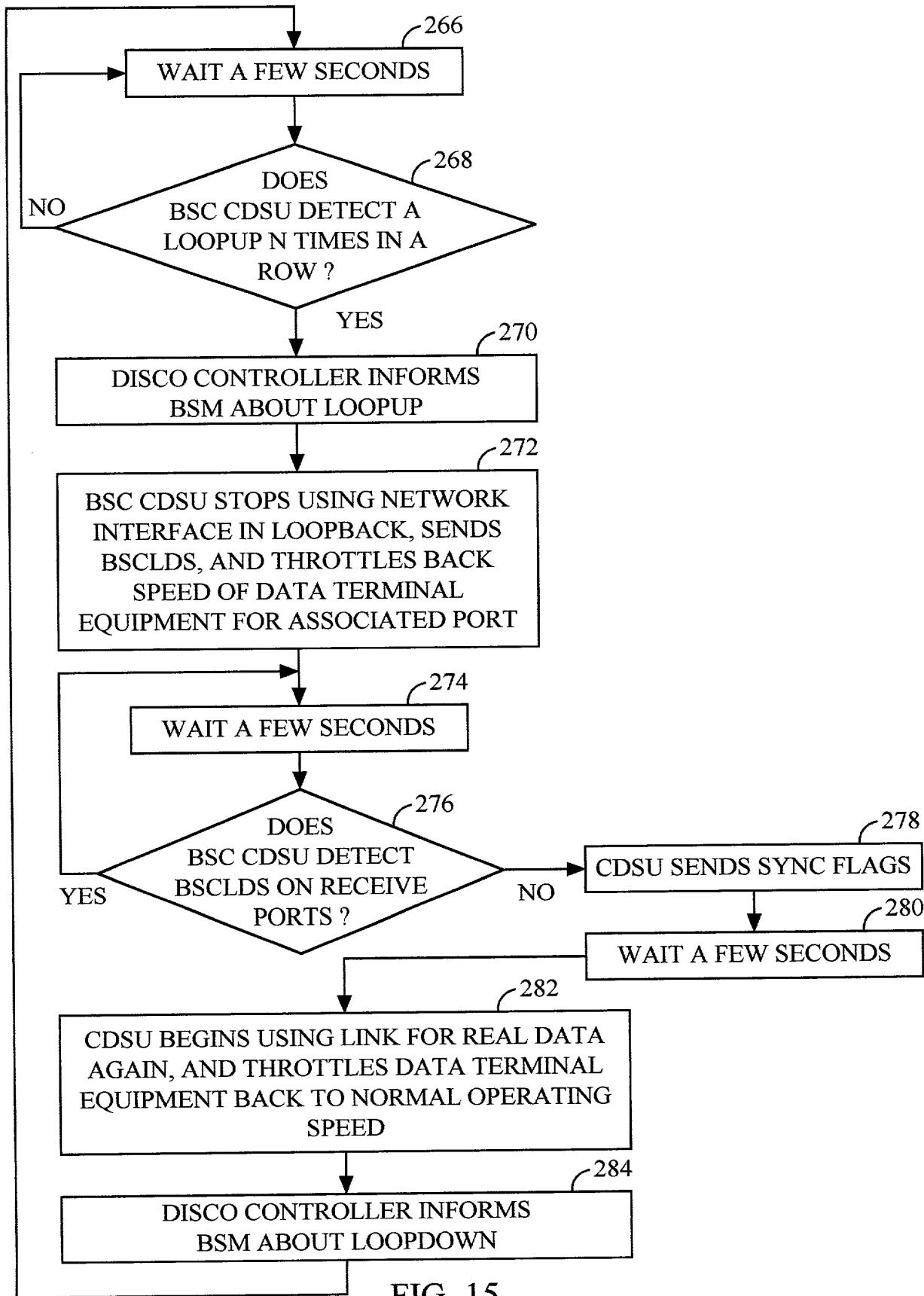


FIG. 15